**WNR (Wireless Neural Recorder)**

Rice University

Weekly Progress Report 26

4/15/2016 - 4/22/2016

**Agenda for meetings**

Mentor Meeting:

1. Go over final specifications and make sure we are on the right track.

**Activities this week**

1. Resting and catching up on other courses
2. Cycle IV Documentation

**Problems encountered**

1. Documentation

**Time devoted to project this week**

|  |  |  |
| --- | --- | --- |
| **Name** | **Tasks Accomplished** | **Hours Spent** |
| Stephen Xia | * Cycle IV Documentation | 7 |
| Tingkai Liu | * Cycle IV Documentation | 7 |
| Xin Huang | * Cycle IV Documentation | 7 |
| Yuan Gao | * Cycle IV Documentation | 7 |
|  | **Team Total** | 28 |

**Meetings Minutes**

Weekly Meeting – 4/22/2016, 12:30PM - 1:30 PM

Attendees: Stephen Xia, Tingkai Liu, Xin Huang, Yuan Gao, Gary Woods

Location: OEDK 104

Completed objectives:

1. Go over design specifications for final review
   1. We meet all specifications, for the most part.
      1. Just have to do the power consumption test and generate power consumed vs. sampling rate
   2. We lost our prototype/pcbs. For the final review, it is good enough just to have a design of the pcb; there is no need to send it out to fab shop and get it made.

**Expenditures**

* N/A

**Action items list**

|  |  |  |  |
| --- | --- | --- | --- |
| **Action item** | **Owner** | **Due date** | **Status** |
| Cycle IV Documentation | Stephen Xia | 4/18/2016 | 100% |
| Cycle V Documentation | Stephen Xia | 5/2/2016 | 0% |
| Power Consumption vs. Sampling Rate Test | Tingkai Liu | 5/2/2016 | 0% |
| New Ideal PCB Design | Xin Huang | 5/2/2016 | 0% |

**Additional Comments/Questions for Mentors**